

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
CASSAGNES)
Serial No. **10/039,233**)
Confirmation No. **7300**)
Filing Date: **December 31, 2001**)
For: **CIRCUIT FOR THE DECODING OF**)
BIPHASE SIGNALS)
)

Examiner: **P. PHU**

Art Unit: **2631**

Attorney Docket No.

01R011554371

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

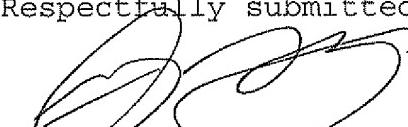
In point 3 of the Reasons for Allowance, the Examiner recites details contained in the specifications. Applicant wishes to clarify that the recited portions of the specifications cannot be read into the allowed claims. The Examiner's comments were merely a demonstrative comparison of an embodiment of the invention including features contained in the allowed claim to an embodiment of the prior art.

Specifically, regarding independent Claims 13, 19, 23, and 30, the Examiner recited that the precharging register is a 4-bit shift register having four D type latches series connected, and in operation, the precharging register precharges respective states of the biphasic signal, one state of the pair of states being precharged at each pulse of a periodic precharging signal in such a way that at each active edge of the period precharging signal, a state of the biphasic signal entered as a least significant bit into the precharging register, and the four bits

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appeared at respective outputs of the four D type latches of the precharging register are given as the precharging registers parallel output for further being inputted into and compared by the verification circuit. Applicant believes that it was not the Examiner's intent to read these recitations from the specification into the scope of Claims 13, 19, 23, and 30 and his statements should not be construed as doing so. Applicant also submits that there is no proper legal basis for reading into the claims the recitations from an embodiment in the specification.

Respectfully submitted,



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